



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **OHASHI, Hitoshi et al.**

Group Art Unit: 1762

Serial No.: 10/047,992

Examiner: **Brian K. TALBOT**

Filed: **January 17, 2002**

P.T.O. Confirmation No.: 5363

For: **METHOD AND APPARATUS FOR FORMING ELECTRIC
CIRCUIT FIXING THE SEMICONDUCTOR DEVICE
USING SUBSTRATE JIG**

AMENDMENT UNDER 37 CFR §1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 22, 2007

Sir:

In response to the Office Action dated **April 20, 2007**, extended from **July 20, 2007** to **October 20, 2007** by a **two (2)** month Petition for Extension of Time, entry of the following amendments and remarks, further examination, and reconsideration are respectfully requested.

Amendments to the Claims begin on page 2 of this paper.

Remarks begin on page 8 of this paper.